



IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

2184  
#3  
cc  
7/15/03

Inventor(s): Timothe LITT

Confirmation No.: 1520

Application No.: 10/034,717

Examiner: Unknown

Filing Date: 12/28/2001

Group Art Unit: 2183

Title: METHOD AND APPARATUS FOR EFFICIENTLY IMPLEMENTING TRACE AND/OR LOGIC ANALYSIS MECHANISMS ON A PROCESSOR CHIP

Mail Stop PGPUB Drawings  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

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JUL 02 2003

Technology Center 2100

DRAWING TRANSMITTAL LETTER

Sir:

Enclosed herewith please find:

- ( ) \_\_\_\_\_ sheets of redlined drawing(s) which indicate proposed changes to the drawing(s). Upon approval of these proposed changes, formal drawing(s) will be submitted.
- ( ) \_\_\_\_\_ sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) which accompanied the Office Action dated \_\_\_\_\_.
- ( ) \_\_\_\_\_ sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) and approved in the Notice of Allowability dated \_\_\_\_\_.
- (X) 5 sheets of formal drawing(s). Please substitute these formal drawing(s) for the informal drawing(s) originally filed.
- ( ) \_\_\_\_\_

Examiner's approval of the entry of these drawings is respectfully requested. No new matter has been added.

Respectfully submitted,

Timothe LITT

By Jonathan M. Harris  
Jonathan M. Harris

Attorney/Agent for Applicant(s)  
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Date: 06/27/2003

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

Date of Deposit: 06/27/2003

Typed Name: Colleen F. Brown

Signature: Colleen F. Brown